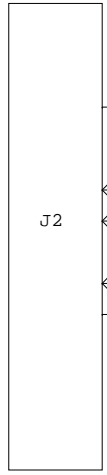
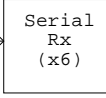


FPGA

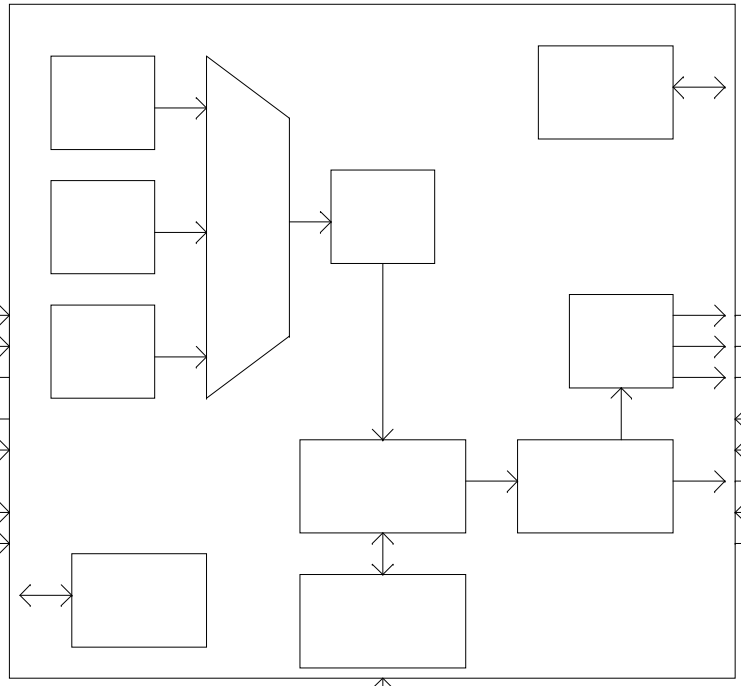
Headboard
Connector
QSH-060-01-L-D-A-RT1



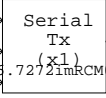
Mipi/HiSpi



Mipi/HiSpi HS
Mipi LP
Controls

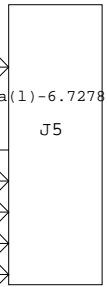


Mipi/HiSpi HS
Mipi LP
Controlp78(L)



Mipi/HiSpi

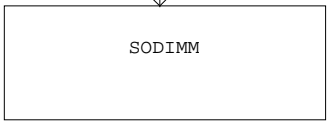
Demo3
Connector
QFS-026-04.25-L-D-A-RT1



Mating Connector:
QTH-060-01-L-D-A-RT1

Recommended Cable:
HQCD-060-12.00-BL-SBR-1-D

SODIMM



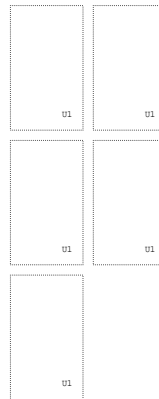
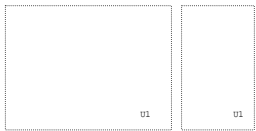
/16 108.81i/

Power - Local Supplies

+0V9 = 2500mA (4000mA)
 +1V5_GX = 220mA (300mA)
 +3.0V = 650mA (1000mA)
 +2.5V_PLL = 500mA (800mA)
 +1.1V = 565mA (800mA)
 +2.5V = 22mA (200mA)
 +1.2V = 6x10mA + 1x20mA = 80mA (200mA)

(300mA)

(4A)



Power Monitor - Local Supplies

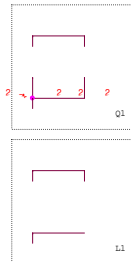
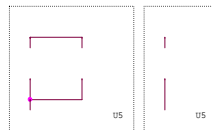
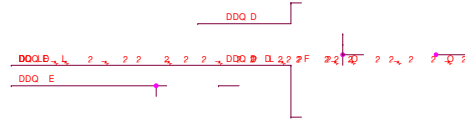
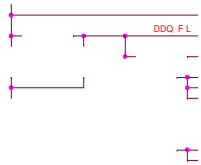
Label "FBD PWR"
 (place on Bottom)

+3V0 (2.70V)
 +2V5 (2.26V)
 +1V2 (1.11)
 +5V0 (3.08)

FPGA Power Status

RST_L	ENABLE	LED
0	0	RED (Supply failure)
1	0	OFF
0	1	AMBER (Non-core supply failure)
1	1	GREEN (Disable option)

Power - SODIMM

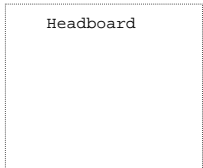
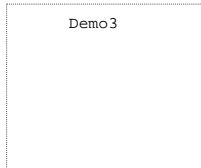


Label "DDR"
 (place on Bottom)

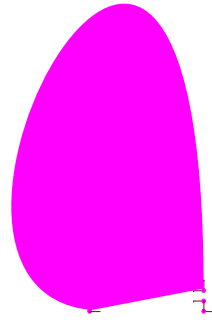
DDR Status

PGOOD	ENABLE	LED
0	0	RED (Supply failure)
1	0	OFF
0	1	AMBER (Calibration failure)
1	1	GREEN (Disable option)

Power Input



Headboard Connector



GX Receive

female top

Demo3 Connector

+2V8 VAA
Demo2 HB Detect (ground if Demo2)
+1V8
+1V2
+2V8

+3V3

PHYSTABLE
RESYNC
TRIGGER

GX Transmit

Oscillators

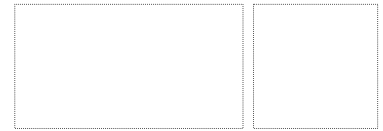
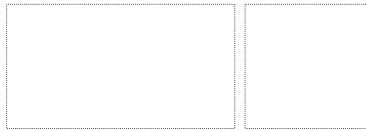
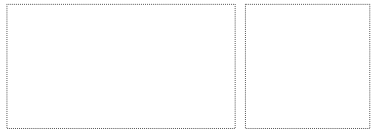
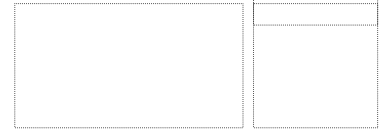
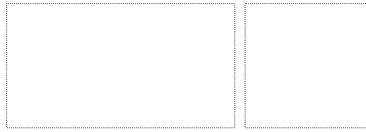
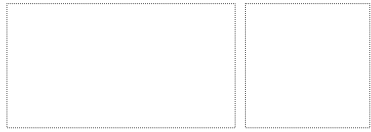


(30mA)



(30mA)





D

D

C

C

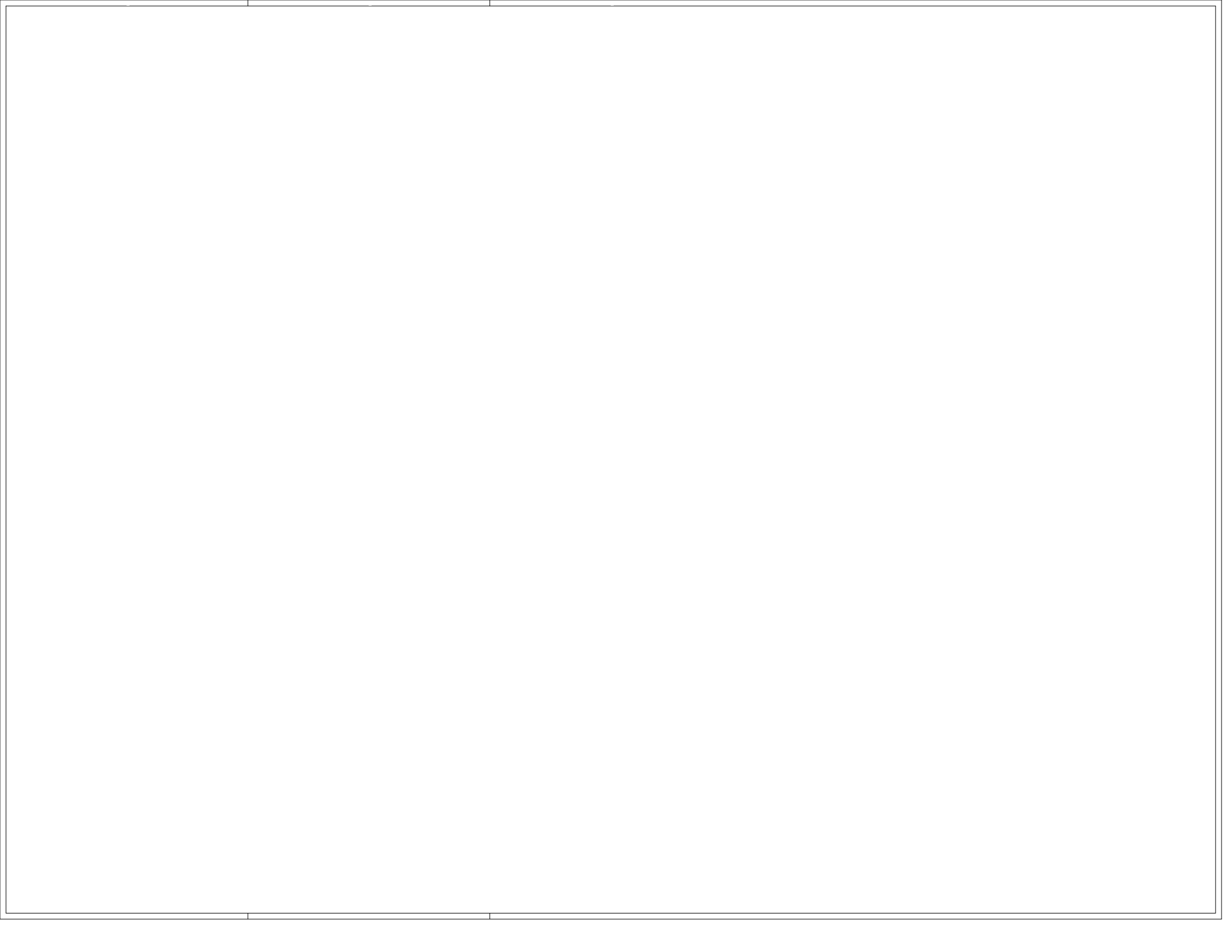
B

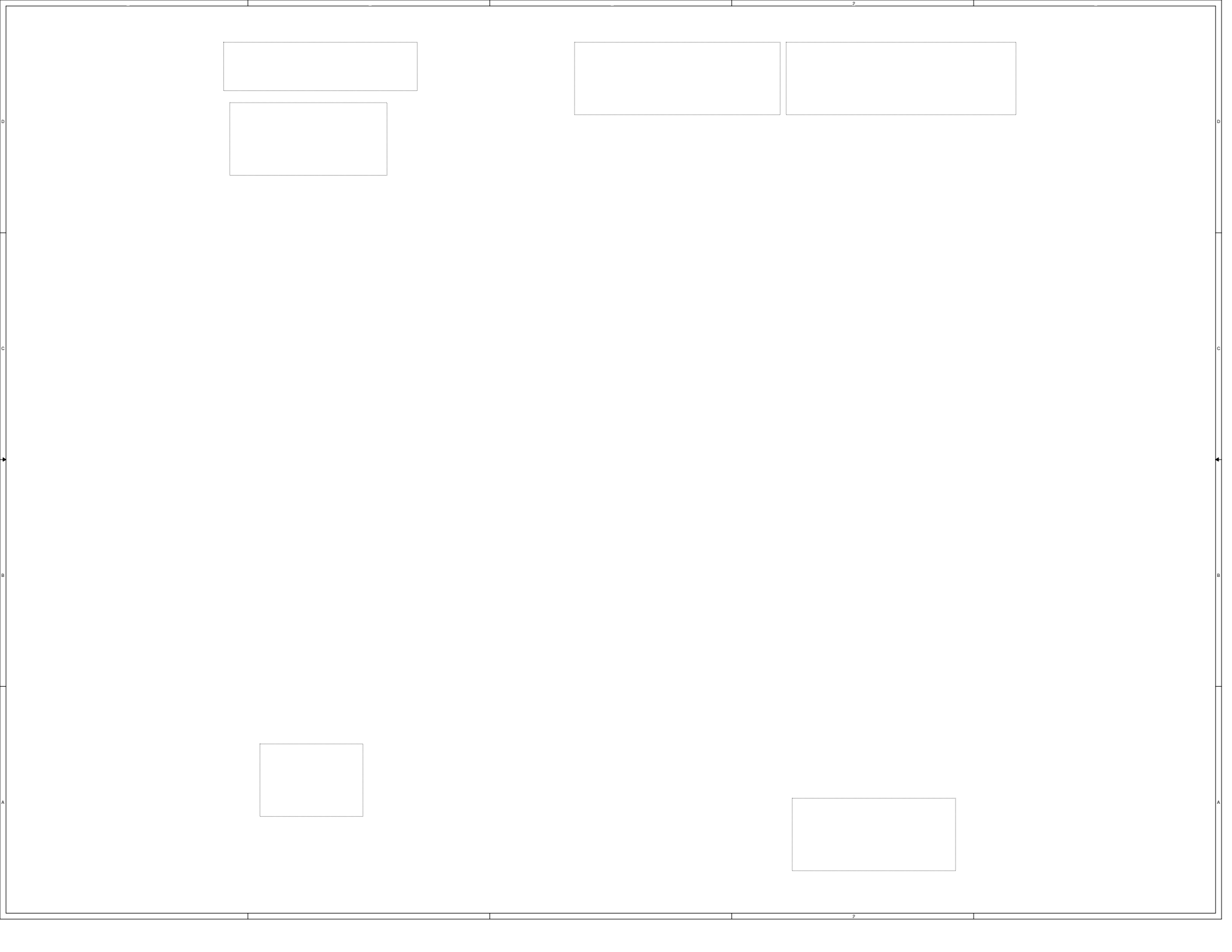
B

A

A







Place Caps near
FPGA pins

Avoid
aggressor signals
for 2.00K Resistors

FPGA PROM

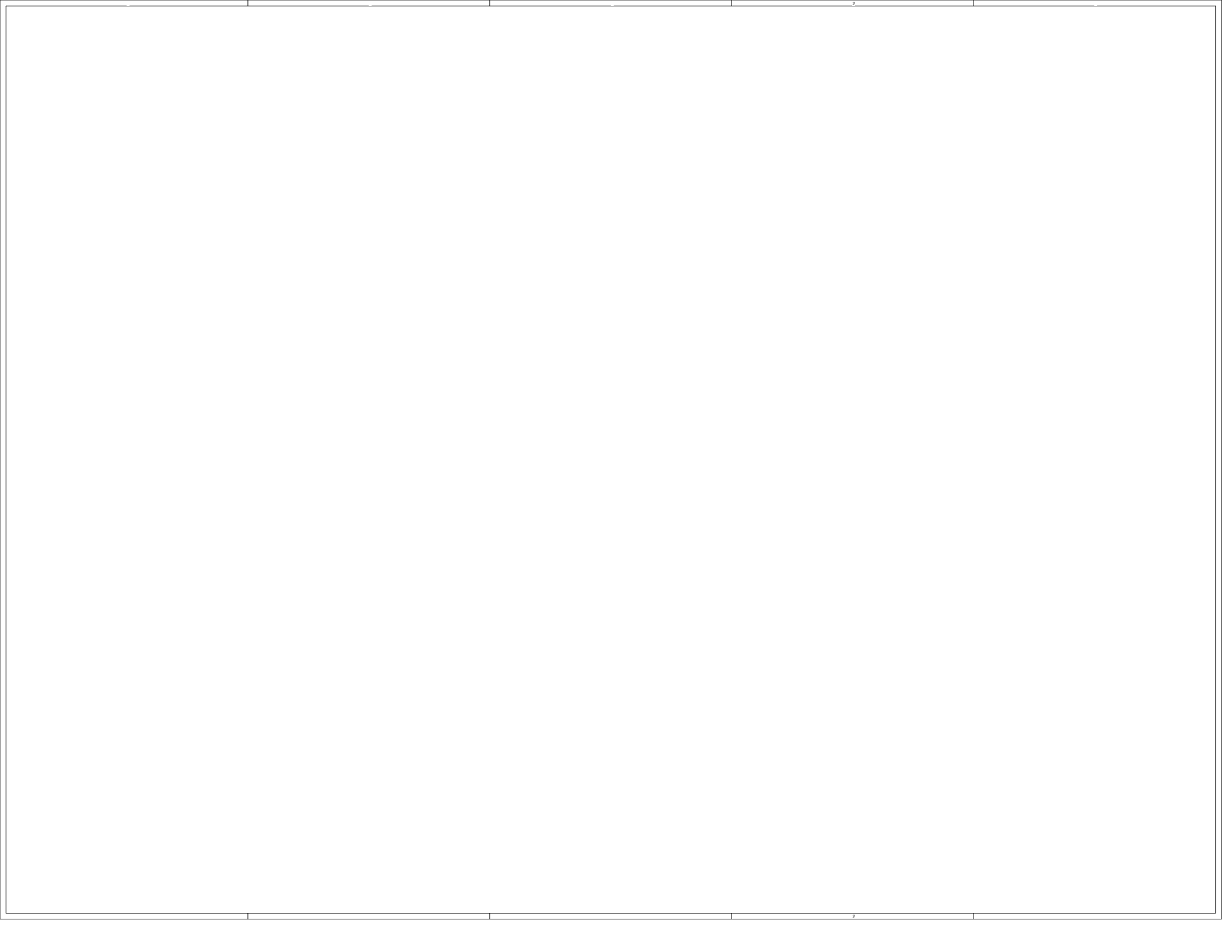


(15mA)

(10mA)



nOE	S	Output
0	0	D3 to Flash
0	1	D3 to HB
1	0	Disabled
1	1	Disabled





(4000mA refresh)

